

Shaik Basheeruddin Shah

Contact Information Room No.: 317, Hostel 2C, Dadri, Shiv Nadar University, Uttar Pradesh, India-201314. **☎**:+91-8826703498, **✉**: bs600@snu.edu.in.

Research Interests Digital Signal Processing, Mathematical Methods for Signal Processing, Graph Signal Processing, and Real-Time Implementation of the Signal Processing Algorithms.

Education **Ph.D. @ Shiv Nadar University** *Aug'2015 -Till date*
Greater Noida, Uttar Pradesh *India*
Doctor of Philosophy in Electronics & Communications Engineering **CGPA : 7.09**
Specialized in *Signal Processing*

- Thesis Topic: *Signal Representations for Period and Frequency Extraction From a Finite-Length Signal.*
 - Advisor: Prof.Vijay Kumar Chakka
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M.Tech @ Rajiv Gandhi University of Knowledge Technologies *2013-15*
IIIT-Nuzvid, Andhra Pradesh *India*
Master of Technology in Electronics & Communication Engineering **CGPA:9.12**
Specialized in *Computational Engineering*

- Thesis Topic: *Time-Frequency Analysis Methods for QRS Delineation in ECG Signal.*
 - Advisor: Mr. V S S K R Naganjaneyulu G, M.E. (IISc).
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B.Tech @ Vasireddy Venkatadri Institute of Technology, *2009-13*
Guntur, Andhra Pradesh *India*
Bachelor of Technology in Electronics & Communications Engineering **74.84 %**

10+2 @Sri Chaitanya Junior College *2007-09*
Guntur, Andhra Pradesh *India*
Intermediate in Mathematics Physics and Chemistry **90.5 %**

SSC @ G.S.R Model Public School *2007*
Secondary School of Education **89 %**

Hardware & Software Skills

- **Programming:** MATLAB, Python, and C.
 - **Hardware:** STM32 and 8051 microcontrollers.
 - **Tools:** LaTeX.
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Teaching Assistance

Rajiv Gandhi University of Knowledge Technologies Sept. 02, 2013 - May 15, 2015
Shiv Nadar University Aug. 3, 2015 - July 31, 2019
Shiv Nadar University (Cum Research Assistant) Aug. 3, 2019 - Till date

Labs Handled

Basic Electrical Engineering (under Prof. R. N. Biswas and Dr. Atul Vir Singh).
Signals & Systems (under Prof. Vijay Kumar Chakka).
Digital Signal Processing (under Prof. Vijay Kumar Chakka and Dr. Madhur Deo Upadhayay)
Graph Signal Processing (under Prof. Vijay Kumar Chakka)
Embedded Systems (under Prof. R. N. Biswas and Dr. Rohit Singh).

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Journal Publications

1. **S. B. Shah**, V. K. Chakka and A. S. Reddy, "Orthogonal and Non-Orthogonal Signal Representations Using New Transformation Matrices Having NPM Structure," in *IEEE Transactions on Signal Processing*, vol. 68, pp. 1229-1242, 2020.
2. **B. S. Shaik**, V. K. Chakka and A. S. Reddy, "A New Signal Representation Using Complex Conjugate Pair Sums," in *IEEE Signal Processing Letters*, vol. 26, no. 2, pp. 252-256, Feb. 2019.
3. **S. B. Shah**, V. K. Chakka and A. S. Reddy, "On Complex Conjugate Pair Sums and Complex Conjugate Subspaces," in *IEEE Signal Processing Letters*, vol. 26, no. 9, pp. 1403-1407, Sept. 2019.
4. **S. B. Shah**, V. K. Chakka and A. S. Reddy, "On Complex Conjugate Pair Sums of Type-2," (under preparation)
5. Srikanth, Goli; Chakka, Vijay Kumar; **Shah, Shaik Basheeruddin**: 'Ramanujan periodic subspace division multiplexing', *IET Communications*, 2019, 13, (15), p. 2296-2303.

Conference Publications

1. **S. B. Shah** and V. K. Chakka, "Signal Representation Using Ramanujan Subspaces Utilizing A Prior Signal Information," 2020 International Conference on Signal Processing and Communications (SPCOM), Bangalore, India, 2020, pp. 1-5.
2. **B. S. Shaik**, V. K. Chakka and Srikanth Goli, "Ramanujan and DFT mixed basis representation for removal of PLI in ECG signal," 2017 4th International Conference on Signal Processing and Integrated Networks (SPIN), Noida, 2017, pp. 509-512.
3. **B. S. Shaik**, V. K. Chakka, Srikanth Goli and A. S. Reddy, "Removal of narrowband interference (PLI in ECG signal) using Ramanujan periodic transform (RPT)," 2016 International Conference on Signal Processing and Communication (ICSC), Noida, 2016, pp. 233-237.
4. **B. S. Shaik** and V. K. Chakka, "Joint reduction of baseline wander, PLI and its harmonics in ECG signal using Ramanujan Periodic Transform," 2016 IEEE Annual India Conference (INDICON), Bangalore, 2016, pp. 1-5.
5. G. V. S. S. K. R. Naganjaneyulu, **B. S. Shaik** and A. V. Narasimhadhan, "R peak delineation in ECG signal based on polynomial chirplet transform using adaptive threshold," 2016 11th International Conference on Industrial and Information Systems (ICIIS), Roorkee, 2016, pp. 856-860.
6. **B. S. Shaik**, G. V. S. S. K. R. Naganjaneyulu and A. V. Narasimhadhan, "A novel approach for QRS delineation in ECG signal based on chirplet transform," 2015 IEEE International Conference on Electronics, Computing and Communication Technologies (CONECCT), Bangalore, 2015, pp. 1-5.
7. **B. S. Shaik**, G.V.S.S.K.R. Naganjaneyulu, T. Chandrasheker, A.V. Narasimhadhan, "A Method for QRS Delineation Based on STFT Using Adaptive Threshold, In *Procedia Computer Science*, Volume 54, 2015, Pages 646-653, ISSN 1877-0509.

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Achievements

- Received meritorious award-**pratibha award** with a **gold medal** for the excellence in M.Tech.
 - Secured **third place** in the preparatory-course conducted by RGUKT- Nuzvid in the fields of MS-office, programming (C-language) and soft skills.
 - Secured **NCC B and C** certificates under the authority of Ministry of Defense, Government of India.
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Workshops

- Delivered a five day Faculty Development Programme (FDP) at **Manakula Vinayagar Institute of Technology, Puducherry** on **signal processing using MATLAB** in December 2019.
 - Delivered several workshops on **MATLAB** basics and discrete-time signal processing for under-graduate students at **Shiv Nadar University**.
 - Delivered a three day workshop at **Vasireddy Venkatadri Institute of Technology, Guntur** on **Introduction to signal processing using MATLAB** in July 2020.
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Membership & Service

- Reviewer of **IEEE Signal Processing Letter (IEEE SPL)**.
 - IEEE Shiv Nadar University student branch chairperson from Aug. 2019 to till date.
 - IEEE student member since 2016.
 - Worked as a volunteer IEEE-National System Conference (NSC)-2015 at Shiv Nadar University.
 - Worked as a **National Service Scheme (NSS)** volunteer from 2009 to 2013.
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Personal Particulars

Nationality: Indian **Gender:** Male
Date of birth: Aug. 21, 1992 **Marital Status:** Unmarried
Languages: **Telugu-** Native, **English-** Written & oral, **Hindi-** Written & oral.
Permanent Address: C/o Shaik Alla Basha & Shaik Chand Bi,
House Number: 2-31/A, Chamallamudi Village,
Vatticherukuru Mandal, Guntur District, **☎:** +91-9492685409.
Andhra Pradesh, India-522017. **✉:** basheershah09@gmail.com.

References

- Prof. Vijay Kumar Chakka (Ph.D Supervisor)
Department of Electrical Engineering, **☎:**+91-9328721441,
Shiv Nadar University. **✉:** Vijay.Chakka@snu.edu.in.
 - Dr. Arikatla Satyanarayana Reddy (Ph.D-Doctoral Committee Chair Person)
Department of Mathematics, **☎:**+91-9953936560,
Shiv Nadar University. **✉:** satyanarayana.reddy@snu.edu.in.
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Declaration

I hereby declare that all the information stated above is true to best of my knowledge and belief. I take the responsibility of any mistakes in data if occur in future.

Shaik Basheeruddin Shah.